Patent

Customer No.: 31561 Application No.: 10/063,737 Docket No. 8727-US-PA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Applicant

: Hsu

Application No.

: 10/063,737 : May 9, 2002

Filed For

: VOLTAGE REFERENCE CIRCUIT LAYOUT INSIDE

MULTI-LAYERED SUBSTRATE

Art Unit

: 2825

Examiner

: BOWERS, BRANDON

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Office Action, mailed on September 23, 2003, Applicant and towould like to elect Group III, claims 16-22, drawn to a multi-layered substrate, classified in class 716, subclass 12. Please cancel claims 1-15 and 23-26 without waiver, prejudice or disclaimer.

Thank you for your actiquestions

questions, please feel free to contact me.

Respectfully Submitted, JIANQ CHYUN IP Office

Date: (100 1 22 2007

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002-1-703-872-9318

TO:

UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: BOWERS, BRANDON

Art Unit: 2825

MESSAGE:

Enclosed please find the Response to Restriction Requirement

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